



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

**HOSHINO, Masataka**

Serial No.: 09/982,963

Group Art Unit: 2823

Filed: **October 22, 2001**

Examiner: **COLEMAN, William D.**

P.T.O. Confirmation No.: **8557**

For: **THREE DIMENSIONAL SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE  
HAVING A PIERCING ELECTRODE (as amended)**

**AMENDMENT UNDER 37 CFR §1.111**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, Va 22313-1450

May 9, 2003

Sir:

In response to the Office Action dated February 10, 2003, please amend the above-identified application as follows:

RECEIVED  
MAY 12 2003  
TECHNOLOGY CENTER 2800

IN THE TITLE:

Amend the title to read as follows:

THREE DIMENSIONAL SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE ~~AND~~  
~~METHOD FOR MAKING THE SAME~~ HAVING A PIERCING ELECTRODE